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Attorney Docket No. 033082 M 336

P A T E N T

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the application of:	)	<b>CONFIRMATION NO.: 8859</b>
	)	
<b>Yasuo KOBAYASHI, et al.</b>	)	
	)	
U.S. Serial No.: 10/585,994	)	Group Art Unit: 2823
	)	
Filed: July 13, 2006	)	Examiner: Kevin A. Parendo

For: **MANUFACTURING METHOD OF SEMICONDUCTOR DEVICE AND FILM DEPOSITION SYSTEM**

**RESPONSE TO RESTRICTION REQUIREMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir :

A response to the Office Action mailed September 25, 2008 is due by October 25, 2008.  
The Action required restriction between two patentably distinct inventions.

Applicants hereby elect without traverse Group I of claims 1-8, drawn to a manufacturing method of a semiconductor device, for examination in this application.

Applicants reserve the right to file divisional application(s) for the non-elected claims in due course. It is submitted that this application now is in condition for examination on the merits and early action in that regard is solicited.

Respectfully submitted,  
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